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SEP 17 2004

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7590 08/26/2004

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J.C. Patents, Inc.  
Suite 250  
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Irvine, CA 92618

09/20/2004 TBESHAW2 00000038 500710 10612607

01 FC:1501	1330.00	DA
02 FC:1504	300.00	DA
03 FC:8001	APPLICATION NO. 9.00	DA

FILING DATE

FIRST NAMED INVENTOR

JIAWEI HUANG

(Depositor's name)

SEPTEMBER 17, 2004

(Date)

10/612,607	07/01/2003	Yun-Sheng Chen	JCLA10931	4794
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**TITLE OF INVENTION: FABRICATING METHOD OF LOW TEMPERATURE POLY-SILICON FILM AND LOW TEMPERATURE POLY-SILICON THIN FILM TRANSISTOR**

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	11/26/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
SOWARD, IDA M	2822		257-638000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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- 1 \_\_\_\_\_  
2 **J.C. PATENTS**  
3 \_\_\_\_\_

## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

AU OPTRONICS CORPORATION

HSTINCHU, TAIWAN

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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Date 09/17/2004Typed or printed name JIAWEI HUANGRegistration No. 43,330

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